



PATENT  
Case Docket No. ALLENG2.001AUS

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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicants : Xu, et al. )  
Appl. No. : 09/737,439 )  
Filed : December 13, 2000 )  
For : INTEGRATED CIRCUIT )  
INDUCTOR STRUCTURE FOR )  
REDUCING DISTRIBUTED )  
CAPACITANCE TO )  
SUBSTRATE )  
Group Art Unit : 2814 )  
Class/Sub-Class : 257/531000 )  
Examiner : Nathan W. Ha )

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10/24/02  
(Date)

John M. Carson, Reg. No. 34,303

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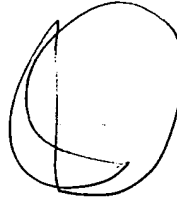
Dear Sir:

Enclosed for filing is the Issue Fee for the above-identified application:

- (X) Form PTOL-85.
- (X) A check in the amount of \$1,610 to cover the issue fee, publication fee, and advance order for ten copies is enclosed.
- (X) One sheet of drawing.
- (X) Comments on Statement of Reasons for Allowance.
- (X) Comments on The Title of The Invention.

07/02

- (X) The Commissioner is hereby authorized to charge any additional fees which may be required, or credit any overpayment, to Account No. 11-1410.
- (X) Return prepaid postcard.



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John M. Carson  
Registration No. 34,303  
Attorney of Record  
Customer No. 20,995  
(619) 687-8632



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PATENT

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#13  
11/12/02  
AGT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants : Xu, et al.  
Appl. No. : 09/737,439  
Filed : December 13, 2000  
For : INTEGRATED CIRCUIT INDUCTOR  
STRUCTURE FOR REDUCING  
DISTRIBUTED CAPACITANCE TO  
SUBSTRATE  
Group Art Unit : 2814  
Class/Sub-Class : 257/531000  
Examiner : Nathan W. Ha

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10/24/02  
(Date)

John M. Carson, Reg. No. 34,303

COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE

United States Patent and Trademark Office  
P.O. Box 2327  
Arlington, VA 22202

Dear Sir:

Applicant wishes to address the Examiner's statement of reasons for allowance in the Notice of Allowability mailed July 30, 2002. Applicant respectfully submits that the claims should be allowed based on the entirety of their verbiage rather than focusing on any specific portion thereof.

Please charge any additional fees, including any fees for additional extension of time, or credit overpayment to Deposit Account No. 11-1410.

Respectfully submitted,

KNOBBE, MARTENS, OLSON & BEAR, LLP

Dated: 10/24/02

By: \_\_\_\_\_

John M. Carson  
Registration No. 34,303  
Attorney of Record  
2040 Main Street  
Fourteenth Floor  
Irvine, CA 92614  
(619) 687-8632

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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants : Xu, et al.  
Appl. No. : 09/737,439  
Filed : December 13, 2000  
For : INTEGRATED CIRCUIT  
INDUCTOR STRUCTURE FOR  
REDUCING DISTRIBUTED  
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Group Art Unit : 2814  
Class/Sub-Class : 257/531000  
Examiner : Nathan W. Ha

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Arlington, VA 22202, on

10/24/02  
(Date)

John M. Carson, Reg. No. 34,303

COMMENTS ON THE TITLE OF THE INVENTION

United States Patent and Trademark Office  
P.O. Box 2327  
Arlington, VA 22202

Dear Sir:

The issue fee transmittal of the Patent Office dated July 30, 2002, indicates that the title of the invention is "Integrated circuit inductor." However, Applicant amended the title of the invention in the Office Action response dated April 15, 2002, from "Integrated Circuit Inductor" to "Integrated circuit inductor structure for reducing distributed capacitance to substrate."

Thus, Applicant respectfully submits that the correct title of the invention for the above-identified application is "Integrated circuit inductor structure for reducing distributed capacitance to substrate."

Appl. No. : 09/737,439  
Filed : December 13, 2000

Please charge any additional fees, including any fees for additional extension of time, or credit overpayment to Deposit Account No. 11-1410.

Respectfully submitted,

KNOBBE, MARTENS, OLSON & BEAR, LLP

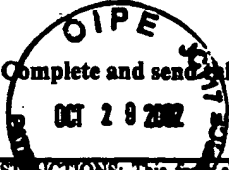
Dated: 10/24/02

By: 

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PART B - FEE(S) TRANSMITTAL

Complete and send this form, together with applicable fee(s), to: **Mail Box ISSUE FEE**  
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**Washington, D.C. 20231**  
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**KNOBBE MARTENS OLSON & BEAR LLP**

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<b>John M. Carson</b>	(Depositor's name)
	(Signature)
<b>10/24/02</b>	(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
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09/737,439 12/13/2000 Shuming Xu ALLENG2.001AUS 1881

TITLE OF INVENTION: INTEGRATED CIRCUIT INDUCTOR STRUCTURE FOR REDUCING DISTRIBUTED CAPACITANCE TO SUBSTRATE

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
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nonprovisional NO \$1280 \$300 \$1580 10/30/2002

EXAMINER	ART UNIT	CLASS-SUBCLASS
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HA, NATHAN W 2814 257-531000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

- ☐ Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached.  
☐ "Fee Address" indication (or "Fee Address" Indication form PTO/SB/47; Rev 03-02 or more recent) attached. Use of a Customer Number is required.

2. For printing on the patent front page, list (1) the names of up to 3 registered patent attorneys or agents OR, alternatively, (2) the name of a single firm (having as a member a registered attorney or agent) and the names of up to 2 registered patent attorneys or agents. If no name is listed, no name will be printed.

- 1 **Knobbe, Martens**  
2 **Olson & Bear, LLP**  
3 \_\_\_\_\_

3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. Inclusion of assignee data is only appropriate when an assignment has been previously submitted to the USPTO or is being submitted under separate cover. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY AND STATE OR COUNTRY)

**Institute of Microelectronics Singapore Science Park II, Singapore**

Please check the appropriate assignee category or categories (will not be printed on the patent) ☐ individual ☒ corporation or other private group entity ☐ government

4a. The following fee(s) are enclosed:

4b. Payment of Fee(s):

- ☒ Issue Fee  
☒ Publication Fee  
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**10/24/02**

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01 FC:1504	300.00 OP
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03 FC:8001	30.00 OP